

India's Trusted Semiconductor Partner, Silicon Patterns, Scales to 200+ Engineers Globally

Hyderabad-based ASIC & SoC design firm crosses 200+ engineers milestone, delivering RTL, UVM Verification, Physical Design, Embedded & AI Silicon across Automotive, IoT and HPC markets for 35+ global clients.



The infographic features a light blue background with a stylized globe on the right and a decorative wave at the bottom left. It includes the Silicon Patterns logo (a head profile with circuit lines), a large '200+' statistic with a people icon, and three key capabilities: Deep Domain Expertise (chip icon), Global Delivery Excellence (globe icon), and Scalable, Reliable, Future-Ready (bar chart icon).

Silicon Patterns
Patterns to your success

India's Trusted Semiconductor Partner

Silicon Patterns, Scales to **200+** Engineers Globally

200+
Engineers Globally

- Deep Domain Expertise
- Global Delivery Excellence
- Scalable. Reliable. Future-Ready.

Hubli, Karnataka May 21, 2026 ([IssueWire.com](https://www.issuewire.com)) - Silicon Patterns Pvt. Ltd., one of India's most trusted custom ASIC and SoC design firms, has scaled its engineering team to over 200 semiconductor professionals—reflecting surging global demand for precision silicon across automotive, AI, IoT, and high-performance computing markets.

Serving 35+ global clients with a 98% customer satisfaction rate and 5+ chip tape-outs per year, Silicon Patterns delivers end-to-end semiconductor engineering from concept to tape-out.

Full-Spectrum Semiconductor Engineering

[Silicon Patterns](#) offers a comprehensive portfolio across the complete chip development lifecycle:

Silicon Engineering — RTL Design, UVM Verification, RTL-to-GDSII Physical Design, DFT, Formal Verification, and Pre & Post Silicon Validation using Synopsys, Cadence, and Siemens EDA toolchains.

Automotive Semiconductor—ISO 26262-compliant design and verification for ADAS, EV powertrain, and infotainment silicon built to the most rigorous functional safety standards.

Embedded Engineering — Bare-metal firmware, BSP, RTOS, Linux drivers, and hardware-software co-design for production-ready embedded systems.

AI Engineering — NPU architecture, ML hardware design, and AI-assisted chip workflows accelerating spec-to-silicon timelines for next-generation intelligent devices.

System Design & Digital Twin of SoCs — SystemC-based high-level modeling enabling parallel development, faster SoC validation, and risk-free architecture exploration before RTL commitment.

Trusted by the World's Most Demanding Chip Teams

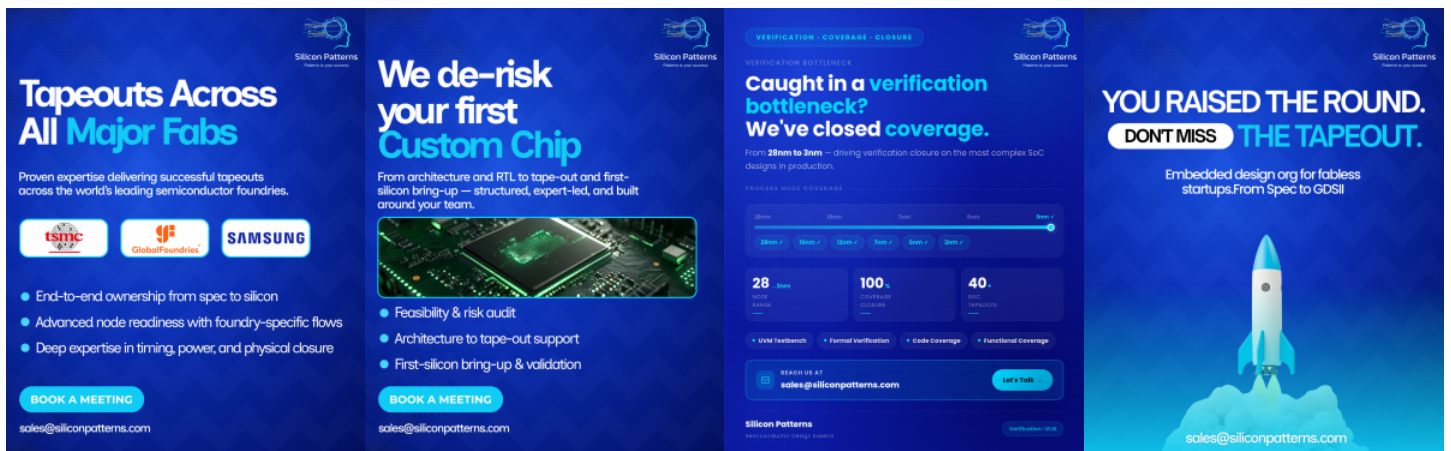
Silicon Patterns' expansion to 200+ engineers is a direct result of the trust placed by global clients who demand nothing less than first-pass silicon success. The company proudly serves six key verticals—Semiconductor Fables, Automotive Electronics, IoT & Smart Devices, Industrial Automation, Consumer Electronics, and AI & HPC.

"Crossing 200 engineers is not just a headcount milestone—it reflects the trust our global clients place in us to solve their hardest silicon challenges," said [Srinivas Gupta Chedella](#), CEO of Silicon Patterns. "We are investing aggressively in our people and processes so our clients achieve first-pass silicon success every time."

India's Growing Semiconductor Footprint

With engineering centers in Hyderabad, Bangalore, Noida, and Raipur, Silicon Patterns serves clients across North America, Europe, and Asia-Pacific. The company's EDA stack includes Synopsys ICC2, Fusion Compiler, ZeBu, Cadence Innovus, Palladium, Mentor Calibre, and Siemens Questasim.

For consultations visit www.siliconpatterns.com or contact info@siliconpatterns.com



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