

Andes Technology and INVECAS Announce Partnership to Win RISC-V-Based SoC Designs for Advanced Proce



San Jose, Nov 16, 2018 (www.issuewire.com) - Andes Offers the Lowest Power, Highest Performance RISC-V IP Cores; INVECAS Contributes Comprehensive IP Libraries, Front- and Back-End Design Expertise and System-Level Board Design

Andes Technology Corporation, a leading supplier of small, low-power, high performance 32-/64-bit embedded CPU cores, and INVECAS, a provider of leading-edge IP solutions, design services and turn-key ASIC capabilities today announced a partnership. The aim of the collaboration is to win RISC-V-based SoC designs for advanced Fab processes. Andes brings to the partnership its extensive RISC-V IP offerings: A25/AX25, N25/N25F, and NX25/NX25F 32- and 64-bit CPU IP cores. Invescas supplies turnkey silicon proven SoC design solutions for diverse market segments, which include IoT, automotive, bio-medical, hand-held devices and computing, among others.

"We are pleased to partner with Andes Technology," said Taher Madraswala INVECAS, EVP & GM. "This partnership will benefit both companies. INVECAS will leverage its design expertise with Andes new RISC-V IP Cores to provide a fast-track for SoC design teams wanting to implement the open-source RISC-V IP core without the delay of coming up to speed on this new open-source CPU instruction set architecture. INVECAS has already implemented the Andes CPU IP cores for control on test shuttles in the most advanced process nodes. We also look forward to the development of platforms for vertical markets in the near future."

"Collaborating with INVECAS is a real coup for Andes Technology," said Emerson Hsiao, Senior VP of Andes Technology USA Corp. "Having a top-tier ASIC design services company helping designers implement RISC-V CPU IP in SoC designs will accelerate adoption on the open source RISC-V ISA, and it will help expand Andes' customer reach. Internet-of-things (IoT) is an application that will benefit from the RISC-V ISA. Andes expertise developing small silicon footprint, low power, high security, and leading-edge performance for the algorithms being developed for IoT have been built into our RISC-V family. INVECAS ability to implement on most fab advanced processes such as 22FDX, 28HPC, 14 or 16nm or even 7nm will extend the capabilities of the 64-bit Andes CPU cores to applications such as AI and Deep Learning"

About INVECAS

INVECAS operates globally from offices in Santa Clara, California, Burlington, Vermont, Bangalore, New Delhi and Hyderabad, India, and in Shanghai and Shenzhen, China. The INVECAS team has a strong track record in delivering world-class semiconductor IPs and its Design and Silicon realization teams have achieved first-time silicon success on multiple designs on leading edge processes. INVECAS brings together an extensive catalogue of High-performance Analog and Interface IP, Foundation IP, coupled with ASIC design and Embedded Software expertise under one roof to provide complete end-to-end ASIC solutions for customers worldwide. For more information, visit www.INVECAS.com.

About Andes Technology Corporation

Andes Technology Corporation is a world class creator of innovative high-performance/low-power 32/64-bit processor cores and associated development environment to serve the rapidly growing global embedded system applications. The company delivers the superior low power CPU cores, including the emerging star RISC-V series with the integrated development environment and associated software and hardware solutions for efficient SoC design. At the end of 2017, the cumulative amount of SoCs containing Andes' CPU IP reaches 2.5 billion. To meet the demanding requirements of today's electronic devices, Andes Technology delivers configurable software/hardware IP and scalable platforms to respond to customers' needs for quality products and faster time-to-market. Andes Technology's comprehensive CPU line includes entry-level, mid-range, high-end, extensible and security families to address the full range of embedded electronics products, especially for connected, smart and green applications. Beginning in 2017, Andes expanded its product line and provides a total solution of RISC-V, the RISC-V series as V5 families processors cores include 32/64-bit N25/NX25 for general purpose, N25F/NX25F for floating-point intensive applications and A25/AX25 for Linux-based applications. For more information about Andes Technology, please visit the [company home page](#).

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